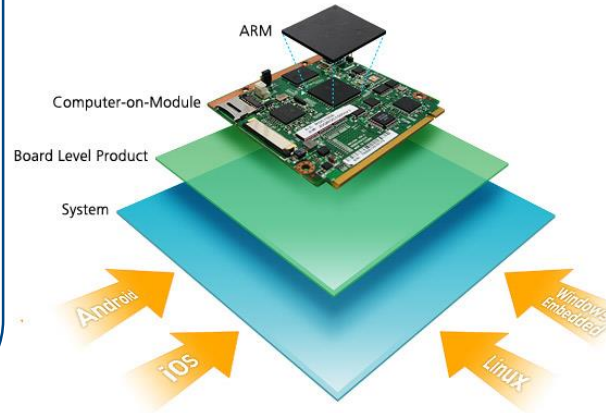


Program:

- **ARM architecture**
- **ARM Cortex M0 SoC : Implementation and simulation**
- **ARM SoC Codesign : Case study, codesign and implementation**



Informations:

- **IP, Tools, Licences and Boards will be delivered**
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